RE-EXAMINATION TIMETABLE, MARCH, 2025
BACHELOR OF TECHNOLOGY (B.Tech.)

THIRD YEAR, SEMESTER-V (for 2021-2022 to 2023-24 Batches Under Autonomy)

Timings: 09:00 a.m. to 12:00 p.m.

Date: 8th March, 2025

Day & Date	Mechanical Engineering	Electronics Engineering	Electronics & Telecommunication Engineering	Information Technology	Computer Engineering	Computer Science & Engineering (Data Science)
Friday, 21/03/2025	Industrial Electronics and Controls	Power Electronics	Microprocessor & Microcontroller	Data Warehousing and Mining	Data Mining and Warehouse	Machine Learning-II (Deep Learning)
Monday, 24/03/2025	Mechanical Vibrations	-	Neural Network & Fuzzy Logic / Operating Systems	Cryptography and Network Security	Processor Organization and Architecture	Information Security
Wednesday, 26/03/2025	Automotive Prime Movers	-	Radio Frequency Circuit Design	Artificial Intelligence	Artificial Intelligence	-
Thursday, 27/03/2025	-	Data Structures and Algorithms	Data Structures & Algorithms	Advanced Data Structures	Advanced Database Management System	-
Friday, 28/03/2025	Heat Transfer	-	Digital Signal Processing	Statistical Analysis	-	-

Important Note: Change if any, in the time table shall be communicated on the College website (www.djsce.ac.in).

Ms. Alka Shukla
Controller of Examinations

Dr. Hari Vasudevan
Principal